UPDATED PROGRAM

8.30  Breakfast Time

9.00  Opening

9.15  Parallelism at Microsoft
      Tony Hey, Microsoft Research, USA

9.45  DOE Directions in Exascale Computing
      Thomas Sterling, Indiana University, USA

10.15 Morning Break

10.45 Collaborative versus Competitive Concurrency: What Control Mechanisms Are Appropriate?
      Morven Gentleman, Dalhousie University, Canada

11.15 The Super Instruction Architecture for High Performance Computing: A Block-oriented DSL and its Runtime System
      Beverly Sanders, University of Florida Gainesville, USA

11.45 e-Science and the Cloud Applications
      Ronald Perrott, Oxford e-Research Centre, UK

12.15 Lunch Break

01.00  An Algorithm for Fast Edit Distance Computations on GPUs
      Roy Campbell, University of Illinois at Urbana-Champaign, USA

01.30  Performance Estimation of Hybrid Systems with FPGAs and GPUs as Coprocessors
      Erik Maehle, University of Luebeck, Germany

02.00  Compilers Should Speak Properties, not just Code
      Alex Shafarenko, University of Hertfordshire, UK

02.30  Fault-tolerant Space-borne Computing
      Hans Zima, University of Vienna, Austria, and Jet Propulsion Laboratory, Caltech, Pasadena, USA

03.00  Afternoon Break

03.30  WG 10.3 Business

05.00  End

07.00  Dinner at Palomino Restaurant Bellevue, 610 Bellevue Way NorthEast